

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet	1	of	2
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Complete if Known

Application Number	10/684,057
Filing Date	October 9, 2003
First Named Inventor	John W. Rapp
Art Unit	2818
Examiner Name	
Attorney Docket Number	1934-14-3

U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

Examiner Signature	<i>Richard B. Franklin Jr.</i>	Date Considered	9/28/05
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 801.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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Substitute for form 1449B/PTO		Complete if Known	
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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		LECURIEUX-LAFAYETTE G. "Un Seul FPGA Depense Le Traitement D'Images", Electronique, CEP Communication, Paris, FR, no. 55, 1998, pp. 98, 101-103	
RBF		VERMEULEN F. ET AL., "Flexible Hardware Acceleration for Multimedia Oriented Microprocessors", Micro-33. Proceedings of the 33 rd Annual ACM/IEEE International Symposium on Microarchitecture. Monterey, CA, Dec. 10-13, 2000, Proceedings of the Annual ACM/IEEE International Symposium on Microarchitecture, Los Alamitos, CA: IEEE Comp. Soc. US, 10 December 2000, pp. 171-177	
RBF		International Search Report for PCT/US 03/34557, December 21, 2004	
RBF		International Search Report for PCT/US 03/34559, January 7, 2005	

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U.S. PATENT DOCUMENTS

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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ - Number ⁴ - Kind Code ⁵ (if known)				
RBF		EP 1 061 438 A1	12/20/00	Hewlett-Packard Company		
RBF		EP 0 945 788 A2	09/29/99	Texas Instruments Incorporated		
RBF		EP 0 945 788 A3	06/26/02	Texas Instruments Incorporated		
RBF		EP 0 694 847 A2	01/31/96	International Business Machines Corporation		

**Examiner
Signature**

Richard B. Franklin Jr.

Date
Considered

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RBF		SALCIC Z ET AL., "FLIX environment for generation of custom-configurable machines in FPLDs for embedded applications", Microprocessors and Microsystems, IPC Business Press Ltd. London, GB, vol. 23, no. 8-9, December 15, 1999, pp. 513-526	
RBF		International Search Report for PCT/US 03/34558, June 16, 2005	

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